

INTERPACK 2023 PROGRAM-AT-A-GLANCE

Pacific Time		Sessions	Speaker/Tech Sessions	Presentation Title	Room
Day 1 - Monday, October 23					
7:00PM	9:30PM			InterPACK Leadership Dinner - By Invitation Only	
3:00PM	6:00PM			Registration Opens	South Foyer
Day 2 - Tuesday, October 24					
8:15 AM	9:15 AM	Plenary 1	Waguih S. Ishak: Glass is a Key Material for the Connected World		Great Room I-III
9:15AM	10:45AM	Tutorial 1	Rack Scale Two-Phase Cooling Systems for High-Performance Computing and AI Clusters		Courtyard I
9:15AM	10:45AM	Tech Sessions	Shutters East I 01-01	Shutters East II 03-01	Shutters West I 04-01 Shutters West II 06-01
10:45AM	11:00AM	Break	Coffee Break / Exhibitors		South Foyer
11:00AM	12:30PM	Panel 1	Thermal/Mechanical/Electrical Challenges of Advanced Mobile/Telecom/Wireless/AI and High-Power Computing Devices		Brickstones
11:00AM	12:30PM	Tech Sessions	Shutters East I 02-01	Shutters East II 05-01	Shutters West I 06-02 Shutters West II 07-01
12:30PM	1:45PM	Plenary 2/Lunch	2023 ASME InterPACK Distinguished Luncheon Speaker Yogendra Joshi: Three Decades of Microsystems Thermal Management Research at DARPA		Gallery
1:45PM	2:45PM	Plenary 3	Peter de Bock: ARPA-E High Risk/High Reward Technology Programs in Heat Transfer and Electronics		Great Room I-III
2:45PM	4:15PM	Workshop 1	Introduction to Robotics, AI and Intel's OpenVINO™ Toolkit		Courtyard II
2:45PM	4:15PM	Panel 2	Printing & Additive Manufacturing Processes for Electronics Packaging		Brickstones
2:45PM	4:15PM	Tech Session	Shutters East I 03-07	Shutters East II 05-06	Shutters West I 05-07 Shutters West II
4:15PM	4:30PM	Break	Coffee Break / Exhibitors		South Foyer
4:30PM	6:00PM	Workshop 2	K16 Mentorship Workshop		Courtyard II
4:30PM	6:00PM	Tech Sessions	Shutters East I 03-02	Shutters East II 04-02	Shutters West I 05-02 Shutters West II 07-02
6:00PM	7:00PM		Exhibitors Presentations and Career Fair		Gallery
7:00PM	8:00PM		Interactive Poster Session		Great Room I-III
8:00PM	8:30PM		K-16 Committee Meeting		Loft I
8:30PM	9:00PM		Journal of Electronic Packaging (JEP) meeting		Loft I
Day 3 - Wednesday, October 25					
8:15AM	9:15AM	Plenary 4	Sumanta Acharya: Thermal Management of Electronics: Research Supported by the National Science Foundation		Great Room I-III
9:15AM	10:45AM	Tutorial 2	Energy Efficient Computing – A Peek Into the Future / Thermal Management of Lithium-Ion Batteries		Courtyard I
9:15AM	10:45AM	Tech Sessions	Shutters East I 02-02	Shutters East II 04-03	Shutters West I 06-03 Shutters West II 07-03
10:45AM	11:00AM	Break	Coffee Break / Exhibitors		South Foyer
11:00AM	12:30PM	Panel 3	Scaling Data Centers Capacity with Advanced Cooling, Computing Architectures and Beyond		Brickstones
11:00AM	12:30PM	Tech Sessions	Shutters East I 03-03	Shutters East II 04-04	Shutters West I 05-03 Shutters West II 06-04
12:30PM	1:45PM	Lunch	Avram Bar Cohen Award InterPACK Allan Kraus Award		Gallery
1:45PM	2:45PM	Plenary 5	Christopher G. Malone: Building a Global Gigawatt-Scale Data Center Fleet - Current and Future Challenges		Great Room I-III
2:45PM	4:15PM	Workshop 3	NIST Road Map Directions to Chip Act (Part 1)		Courtyard II
2:45PM	4:15PM	Tutorial 3	Opportunities and Challenges in Co-Packaged Si Photonics		Courtyard I
2:45PM	4:15PM	Tech Session	Shutters East I 03-08	Shutters East II 05-08	Shutters West I 05-09 Shutters West II
4:15PM	4:30PM	Break	Coffee Break / Exhibitors		South Foyer
4:30PM	6:00PM	Workshop 4	NIST Road Map Directions to Chip Act (Part 2)		Courtyard II
4:30PM	6:00PM	Tech Sessions	Shutters East I 02-03	Shutters East II 03-04	Shutters West I 06-05 Shutters West II 07-04
6:00PM	6:30PM		InterPACK Meeting (Open)		Loft I
6:30PM	7:00PM		InterPACK Meeting (Closed)		Loft I
7:00PM	7:30PM		EPPD Meeting		Loft I
Day 4 - Thursday, October 26					

8:15AM	9:15AM	Plenary 6	Vincent Kim: Exciting Work Toward Advanced Packaging			Great Room I-III
9:15AM	10:45AM	Tutorial 4	Electric Motor and Integrated Traction Drive Thermal Management			Courtyard I
9:15AM	10:45AM	Tech Sessions	Shutters East I	Shutters East II	Shutters West I	Shutters West II
			01-02	02-04	05-04	06-06
10:45AM	11:00AM	Break	Coffee Break / Exhibitors			South Foyer
11:00AM	12:30PM	Panel 4	AI for the Thermal Science Community			Brickstones
11:00AM	12:30PM	Tech Sessions	Shutters East I	Shutters East II	Shutters West I	Shutters West II
			02-05	03-05	04-05	06-07
12:30PM	1:45PM	Lunch	InterPACK & Nasser Grayeli Poster, EPPD & JEP Awards			Gallery
1:45PM	2:45PM	Plenary 7	John Lau: Chiplet Design and Heterogeneous Integration Packaging			Great Room I-III
2:45PM	4:15PM	Workshop 5	Ultra-Wide Bandgap Device Electro-Thermal Co-Design Workshop			Courtyard II
2:45PM	4:15PM	Panel 5	Women in Engineering			Brickstones
2:45PM	4:15PM	Tech Session	Shutters East I	Shutters East II	Shutters West I	Shutters West II
			03-09	05-10		
4:15PM	4:30PM	Break	Coffee Break / Exhibitors			South Foyer
4:30PM	6:00PM	Tech Sessions	Shutters East I	Shutters East II	Shutters West I	Shutters West II
			03-06	04-06	05-05	07-05
6:00PM			CONFERENCE ENDS			