



ASME InterPACK 2022

International Technical Conference on Packaging and Integration of Electronic and Photonic Microsystems

Conference: October 25 – 27, 2022

The Hyatt Regency Orange County, Garden Grove, CA



Since 1992, InterPACK has been a premier international conference for the exchange of state-of-the-art knowledge in research, development, manufacturing, and applications of electronics packaging and heterogeneous integration. It is the flagship conference of the ASME Electronic and Photonic Packaging Division (EPPD). InterPACK is a systems-focused conference covering topics on Heterogeneous Integration, Servers of the Future, Edge and Cloud Computing, Internet of Things, Additive Printed Electronics, Flexible and Wearable Electronics, Photonics and Optics, Power Electronics, Energy Conversion and Storage, and Autonomous, Hybrid and Electric Vehicles. The international nature of the meeting has been highly beneficial in promoting global interactions between industry, academia, research institutions, funding agencies, start-ups and entrepreneurs. In addition to paper presentations and exhibits, InterPACK 2022 will include panel discussions, workshops, tutorials, keynote and technology talks by prominent speakers, and a joint industry, national laboratory, and academia poster session

Plenary Speakers



Thomas Kazior
Program Manager
DARPA



Philseok Kim
Program Director
ARPA-E



Abhinav Saxena
Principal Scientist
GE Research



Mark Spector
Program Officer
ONR



Ivor Barber
VP of Packaging
AMD

Workshop dedicated to CHIPS and Science act

If you are interested in how the CHIPS and Science Act will strengthen domestic semiconductor manufacturing, design and research, join ASME InterPACK and get the latest news from industry and academia representatives

More information about ASME InterPACK 2022 you can find here:

<http://event.asme.org/interpack>





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Technical Tracks

Heterogeneous Integrated Systems <ul style="list-style-type: none"> ▪ Advanced packaging ▪ Die stacking ▪ Multichip modules ▪ Through-silicon vias (TSV) ▪ System in package (SiP) 	Multi-scale Thermal Transport, Thermal Materials, and Energy Systems <ul style="list-style-type: none"> ▪ 1D and 2D nanomaterials ▪ Nano-scale conjugate heat transfer ▪ Renewable energy ▪ Energy storage ▪ Multi-scale energy transport
Data Centers, Servers of the Future, Edge and Cloud Computing <ul style="list-style-type: none"> ▪ Thermal and infrastructure management ▪ Rack level cooling ▪ Hardware thermal management ▪ Thermal interface materials ▪ Advanced cooling technologies 	Harsh Environment Electronic Applications for Transportation Systems <ul style="list-style-type: none"> ▪ High temperature sensors ▪ RADAR ▪ Advanced driver assistance system ▪ MEMS packaging ▪ Nano-satellites
Flexible, Wearable and Additively Printed Electronics <ul style="list-style-type: none"> ▪ Printed & additive multi-layer electronics ▪ Wearables ▪ Internet of things (IoT) ▪ Flexible electronics for automotive, space, and RF applications 	Reliability of Electronic Packages and Systems <ul style="list-style-type: none"> ▪ Packaging reliability ▪ Electromigration ▪ Reliability of power electronics packaging ▪ Reliability testing for harsh environment ▪ Reliability modeling and simulation
Photonics and Optics <ul style="list-style-type: none"> ▪ Photonics packaging ▪ Optical integration ▪ Thermal/mechanical challenges ▪ LED Systems ▪ High speed transceivers 	Digital Technologies in Microelectronics <ul style="list-style-type: none"> ▪ Exaflop computing systems ▪ Artificial intelligence (AI) ▪ Machine learning (ML) ▪ Digital twin ▪ Quantum computing
Power Electronics <ul style="list-style-type: none"> ▪ Wide bandgap electronic devices ▪ Solid state technologies ▪ Multi-physics co-design ▪ Thermal management and packaging ▪ Harsh environment sensors 	Interactive Presentations <ul style="list-style-type: none"> ▪ Industry networking ▪ Showcase leading-edge technologies ▪ Direct feedback from professionals ▪ Industry mentorship ▪ Best poster award competition

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Tutorials, Workshops and Keynote Speakers

Topic	Speaker
Nanophotonic Systems: Novel Nanophonics Device and Future Applications in Electronics <i>(Tutorial)</i> Organized By: Anil Yuksel , IBM Corp.	Jason Valentine , Vanderbilt University
Advance Liquid-Cooling Technology for ICT: Performance and Economic Benefits <i>(Tutorial)</i> Organized By: Roanld Warzoha , US Naval Academy	Raffaele-Luca Amalfi , Segunte Tech.
Optimization of Battery Recycling for Sustainability <i>(Tutorial)</i> Organized By: Nenad Miljkovic , UIUC.	Ilias Belharouak , Oak Ridge National Lab. Zheng Chen , UC -San Diego
Introduction to Robotics, AI, and Intel's OpenVINO Toolkit <i>(Workshop)</i> Workshop Organized By: Anna Prakash , Intel Corp.	Anna Prakash , Intel Corp. Christine Stewart , Intel Corp.
Managing Data Center Challenges in the Age of AI <i>(Keynote)</i> Organized By: Gamal Refai-Ahmed , AMD	Ali Heydari , Nvidia
Novel ZTACH ACE: Anisotropic Conductive Epoxy for High-Volume <i>(Keynote)</i> Organized By: Benjamin Leever , Air Force Research Lab.	John Yundt , Sunray Scientific
Form and Function in Power Electronics: Driving Progress in Efficient Energy Conversion by Matched Technology-Topology Integrated Design <i>(Keynote)</i> Organized By: Gilbert Moreno , NREL and Emre Gurpinar , Siskorsky Aircraft	Prof. Alberto Castellazzi , Kyoto University of Advanced Science
Past and Future of Reliability Assessment of Electronic Materials <i>(Keynote)</i> Organized By: Pradeep Sharma , Univ. of Houston	Patrick McCluskey , Univ. of Maryland
Building Explainable AI for Enterprise <i>(Keynote)</i> Organized By: Azeem Sarwar , General Motors	Krishna Gade , Fiddler AI

More information about keynotes for ASME InterPACK 2022 can be found here: <https://event.asme.org/InterPACK/Speakers/Track-Keynotes>



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Panel Discussions

Panel Topic	Panelist
<p>Impact of Sustainability Goals on Future Hardware and Data Center</p> <p>Organized By: Cheng Chen, Meta Inc.</p>	<p>Tiffany Jin, Meta Inc. Jimil Shah, TMGcore Peter de Bock, Dept. of Energy. Pritish Parida, IBM Ali Heydari, Nvidia</p>
<p>Pathfinding to Maximize System Performance</p> <p>Organized By: Gamal Refai-Ahmed, XILINX Corp.</p>	<p>Mr. Ivor Barber, AMD Prof. Bahagat Sammakia, SUNY Binghamton Ravi Mahajan, Intel Prof. Al Ortega, Villanova University</p>
<p>Hybrid and System Level Integration for Electronics Packaging</p> <p>Organized By: Benjamin Leever, US Air Force.</p>	<p>Nancy Stoffel, GE Global Research Mark Poliks, SUNY Binghamton Jaim Nulman, Nanodimension Inc Suresh Sitaraman, Georgia Tech</p>
<p>The Importance of Sustainable Energy Systems</p> <p>Organized By: Luca Amalfi , Segunte Tech.</p>	<p>Prof. Nenad Miljkovic, UIUC Filippo Cataldo, Wieland Provides Cosimo Pecchioli, Alpha Laval Prof. Aaron Wemhoff, Villanova Univ.</p>
<p>Thermal and Mechanical Challenges and Opportunities of Advanced Mobile/Telecom/IoT/Auto/Computing Devices</p> <p>Organized By: Victor Chiriac, GCTG LLC.</p>	<p>Sreekant Narumanchi , NREL Raffaele Luca Amalfi ,Segunte Tech. Prof. Amy Marconnet , Purdue Sam Zhao, Broadcom Christopher Kapusta, GE Research Kinzy Jones, Google Inc</p>
<p>Reliability of Additively Manufactured Electronics</p> <p>Organized By: Patrick McCluskey, U. of Maryland.</p>	<p>Prof. David Huitink, U. of Arkansas Prof. Abhijit Dasgupta, U. of Maryland Prof. Siddhartha Das, U. of Maryland</p>
<p>Women in Engineering</p> <p>Organized By: Anna Prakash, Intel Corp.</p>	<p><i>To be confirmed</i></p>
<p>Why AI/Data Science Projects Fail</p> <p>Organized By: Azeem Sarwar, General Motors</p>	<p>Joyce Weiner, Intel Taimoor Khawaja, Cox Communications Syed Usman Ali, Bloomberg Krishna Gede, Fiddler AI</p>
<p>Heterogeneous Integration</p> <p>Organized By: Ravi Mahajan, Intel Corp (to be confirmed)</p>	<p><i>To be confirmed</i></p>